

Notice of References Cited

Application/Control No.

09/761,600

Applicant(s)/Patent Under
Reexamination
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Examiner

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Art Unit

2123

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,161,081	12-2000	Hashmi et al.	703/14
	B	US-			
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	K	US-			
	L	US-			
	M	US-			

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*	V	Lutter et al, "Using VHDL for Simulation of SDL Specifications", Proceedings of the Conference on European Design Automation, pages 630-635, 1992
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.